GUJCOST Sponsored

Scholars' Day

in

Information

& Communication Technology

September 19, 2015

Organized by





Dept. of Electronics & Communication Engg., and IEEE DDU Student Branch, Faculty of Technology, Dharmsinh Desai University, Nadiad-387001. Gujarat.



Patron

Dr. H. M. Desai, Vice Chancellor, D. D. University

Vice-Patron

Prof. D. G. Panchal Dean, Faculty of Technology

Convener

Dr. Nikhil Kothari Head, Dept. of Electronics & Communication Engg.

Coordinators

Dr. Prarthan Mehta Prof Shital Thakkar
Associate Professor, Dept. of E & C Engg.,
Faculty of Technology,
D. D. University,
Nadiad-387001. Gujarat.

Contacts

mehtaprarthan.ec@ddu.ac.in 9427384610 shitalthakkar.ec@ddu.ac.in 9426245608

Registration Fee (Participants/Viewers): Rs. 100.00

D.D./Cheque should be drawn in favor of *Dharmsinh Desai University* payable at Nadiad

Motivation & Objective:

Number of Institutions offering technical education has grown tremendously in last few years within India in general and Gujarat in particular. Both, state and central government have made strong efforts for supporting higher education and encouraging research & development activities. This has made a large number of young faculty members of technical Institutions conscious about their role in technological advancements and carrier prospects. As a result, there has been continuous rise in the number of Ph.D. registrations at different Universities.

In view of the fact that majority of the Institutions and even some of the Universities are recently established, there is a serious shortfall of experienced researchers/experts to help the Ph.D. /M. Tech. Scholars in emerging fields like Electronics, Communication, Computer Science or Information Technology, i.e. ICT. The environment to facilitate the young research scholars is evolving. In order to support the conducive environment for research, the young scholars are required to be familiar with current trends, importantly, technological issues, research methodology, and tools to conduct the experiments related to their research.

The program is an attempt to provide a common platform to share and discuss individual research and methodology of the experimentation in the different fields of ICT.

Program:

Presentations related to the following tracks are invited from the interested research scholars:

Track-1: Electronics, Signal Processing & Embedded Systems

- CMOS/VLSI Design & Technology
- MEMS
- Embedded System Design & Development
- Digital Signal/Image Processing

Track-2: Advanced Communication Systems

- Digital & Analog Communication System
- Wireless & Wired Communication System Design & Technologies
- Microwave Engineering
- Antenna Design & Technology

Track-3: Information Technology

- Data Mining
- Service Oriented Architectures
- Artificial Intelligence
- Machine Learning
- Formal Language Theory
- RTOS

Track-4: Data Communication & Networking

- Wired & Wireless Network Design & Technologies
- Network Protocols
- Distributed Computing
- Grid Computing
- Cloud Computing

Note: The Best Presentation in each track will be awarded.

Guidelines for Presentations:

a. Slide Show:

State of art in technology, Objective of research (Ph. D. or M. Tech. dissertation), Comparison of platforms or tools available and used, Methodology (with test-bed), Interpretation of the obtained results

b. Demonstration:

Assembled/real/simulated test-bed with configuration parameters and sample results

Note: a. & b. both are compulsory (The content need not disclose the research hypothesis/idea, if not published)

Interested Scholars should send a copy of abstract containing motivation & objective of the research, along with the block diagram of the methodology adopted for the experimentation (with the name of the tool used), through E-mail, on or before July 31, 2015. The abstract should be typed in a single column format with double line spacing, TNR font size 12, in A4 sized paper. Maximum length of the same is 2 pages.

Notification of acceptance through E-mail: August 15, 2015.

Send the filled registration form with the registration fees, by August 24, 2015.

Interested students/scholars can also register as viewers.



Name:





GUJCOST Sponsored

Scholars' Day in Information

&

Communication Technology

(Organized by IEEE DDU Student Branch)

September 19, 2015.
Registration Form

Designation:
Organization:
Registration: Participant / Viewer
Mobile No.:
-mail:
Mode of Fees Payment: Cash/Cheque/DD. Details (if required):
Date: Place:
iignature: